





SBEV-RIN32M4CL3 Rev2.00

SCHEMATIC Revision 1.00 = PWB Revision 1.00

Date	Rev	Page	Changes	変更作業者
2021/09/22	2.00	2	Correct pin assignment from Y12 to U17 of U1A.	伊藤

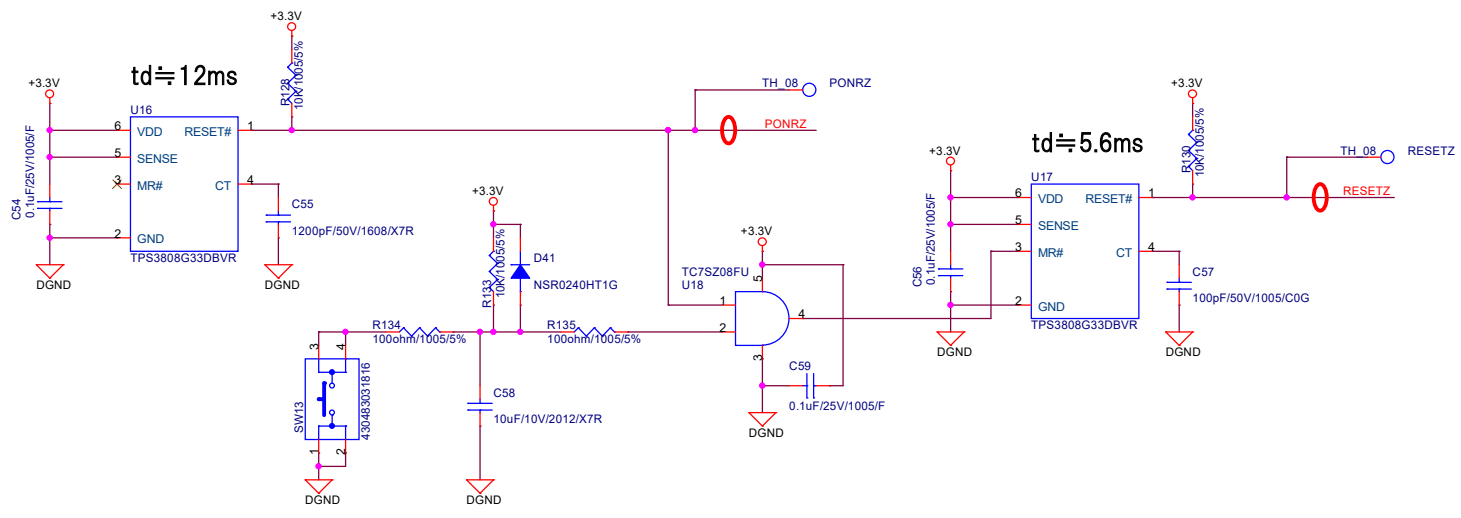
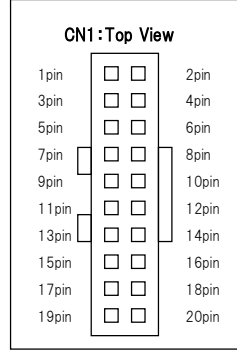
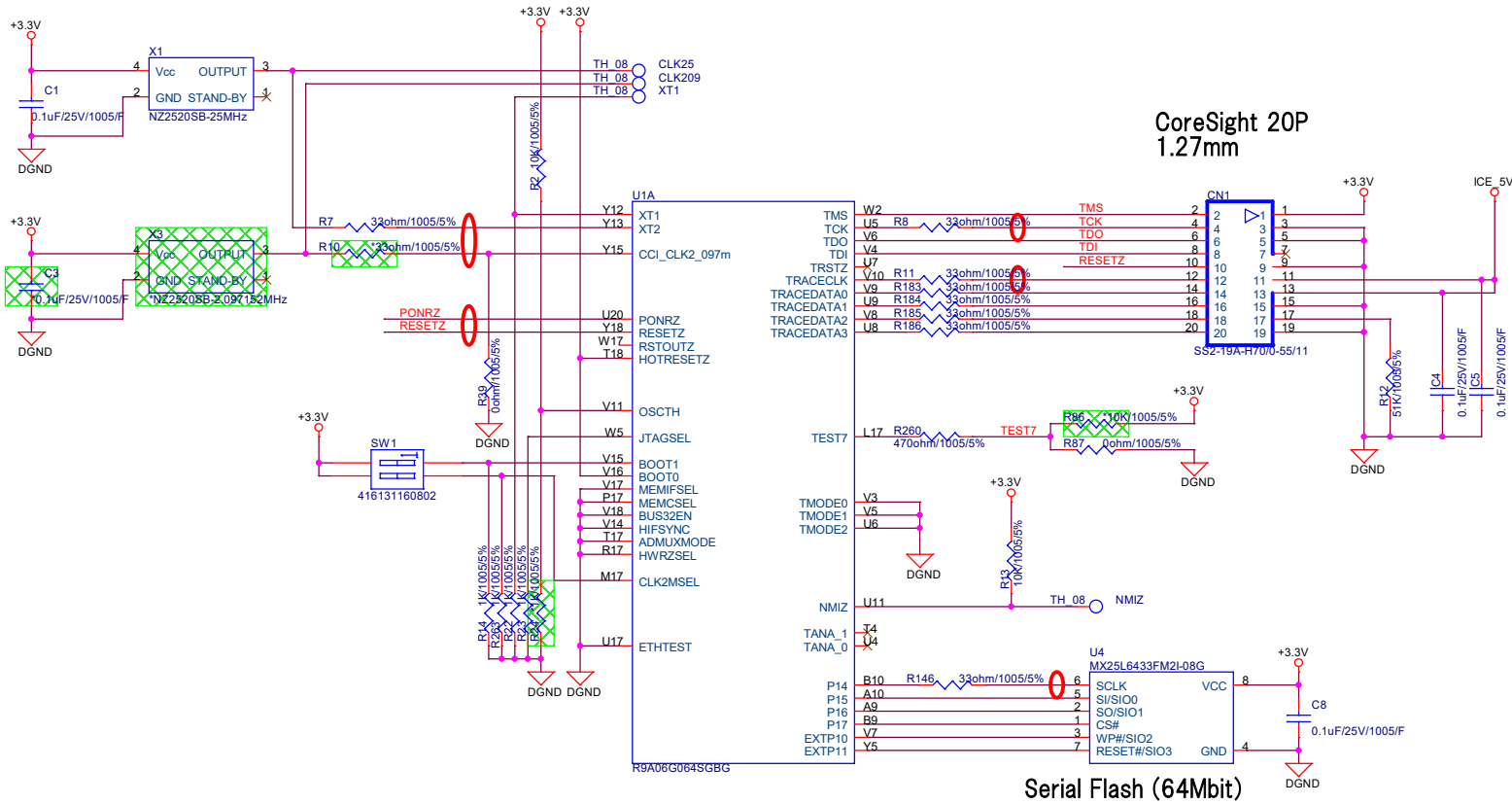
注意事項

*印はGNDガード  *印は等長配線  *印はインピーダンス指定 

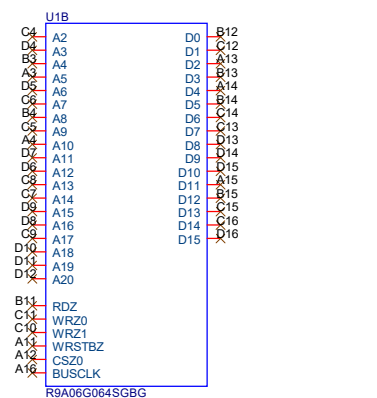
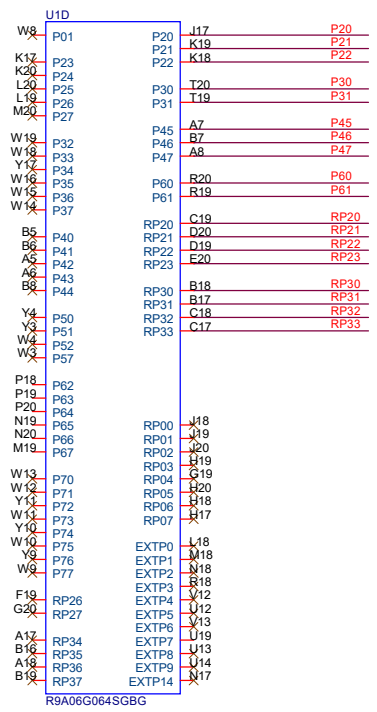
*印は未実装 

Rev1.00 205 Parts, 25 Library Parts, 151 Nets, 771 Pins

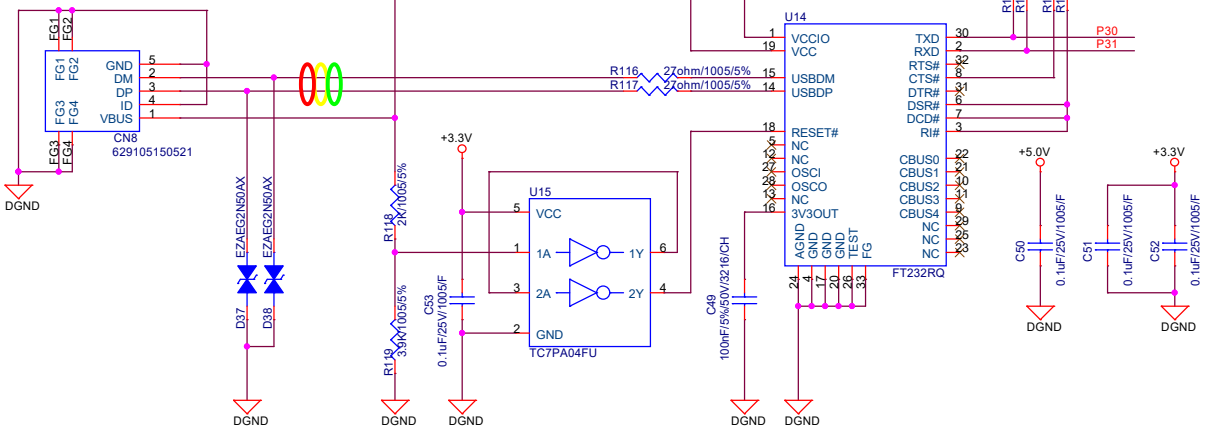
TOP			
Title			
SBEV-RIN32M4CL3			
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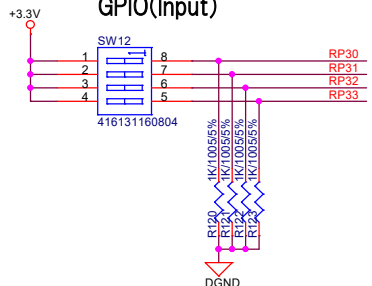
CPU		
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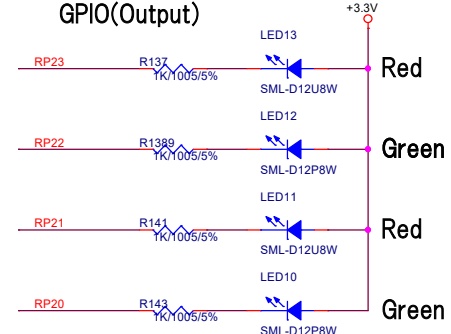
UART



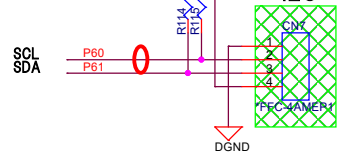
GPIO(Input)



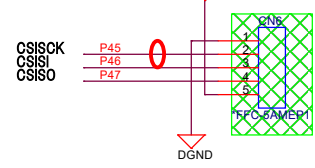
GPIO(Output)



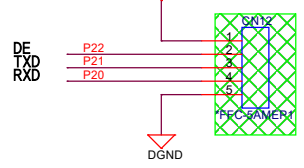
I2C



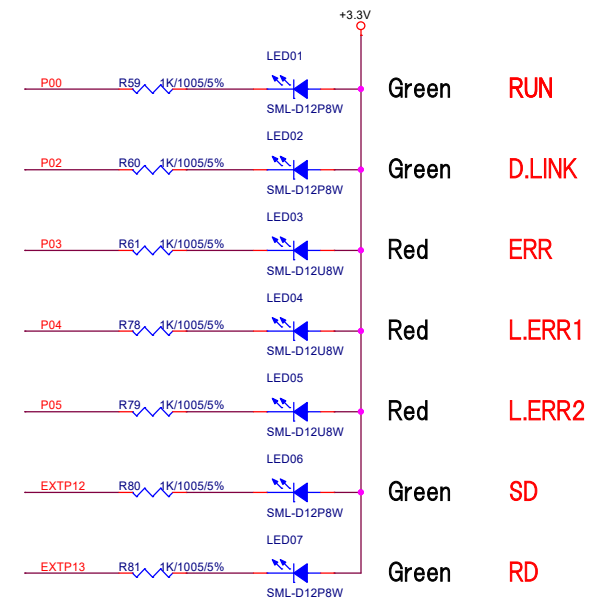
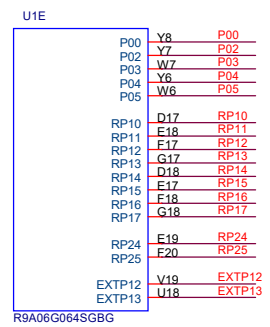
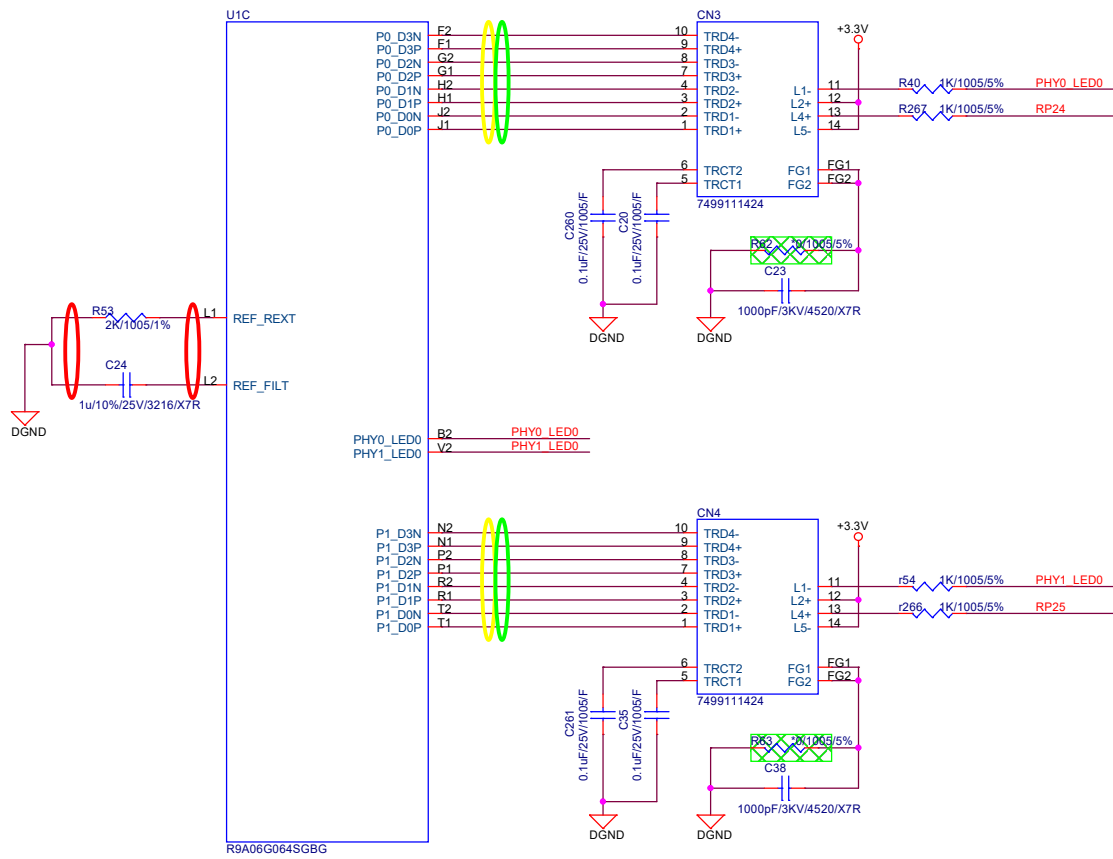
CSI



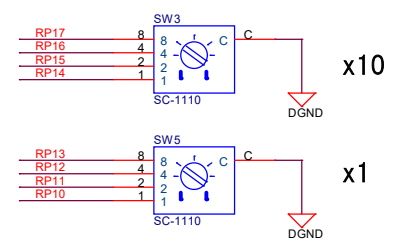
RS-485



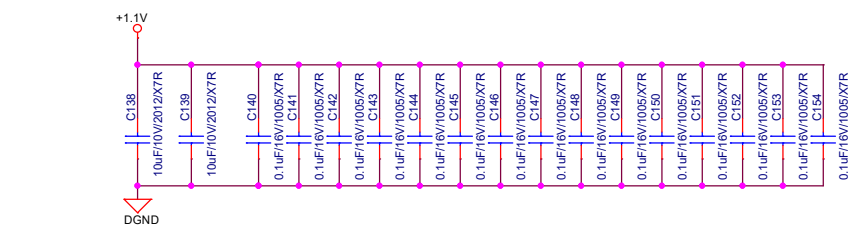
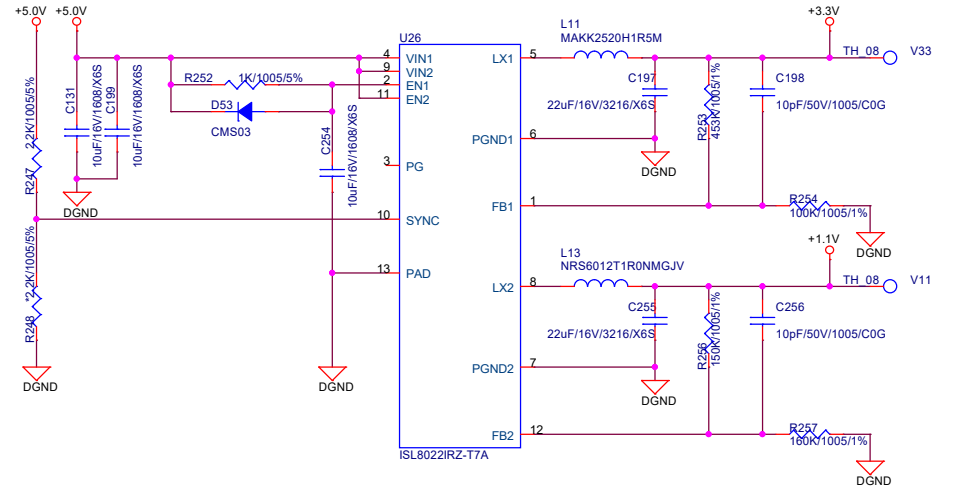
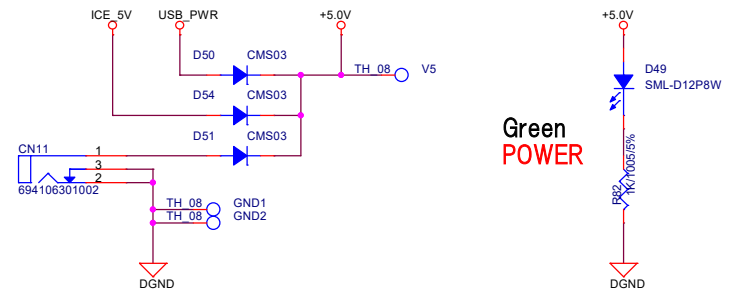
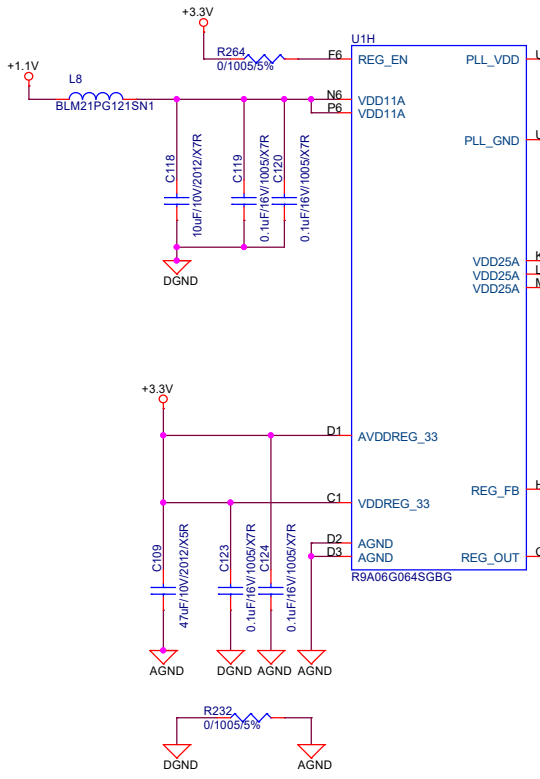
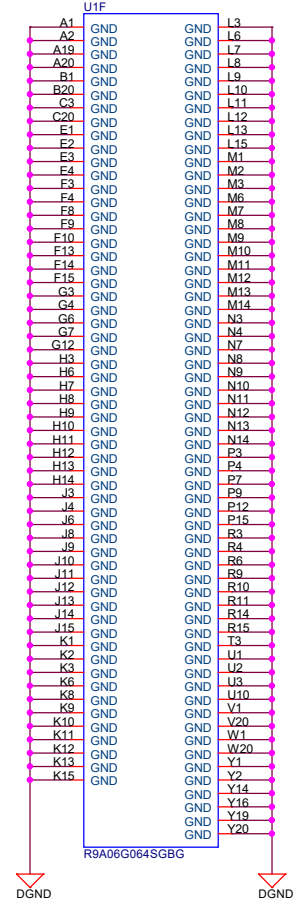
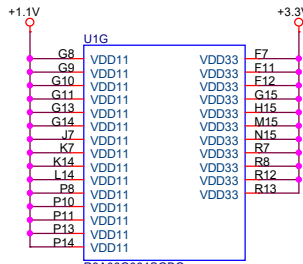
IO		
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Station/IP



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Power		
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